## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re. l	U.S. Patent Application of	)				
HASE	GAWA et al.	)				
		)	<b>Unit 2813</b>			
Appli	cation Number: 10/058,787	)				
	·	)	Examiner			
Filed:	January 30, 2002	)	Dolan, Jennif	er M		
	·	)				
For:	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE,	)				
	METHOD OF TESTING SEMICONDUCTOR	)				
	INTEGRATED CIRCUIT DEVICE AND METHOD OF	)				
	MANUFACTURING SEMICONDUCTOR INTEGRATED	)	9			
	CIRCUIT DEVICE	)	يا د د د د د د د د د د د د د د د د د د د	. J.		
		)	•	王		N
ATTO	RNEY DOCKET NO. ASAM.0038	)	•	5	SET .	
				75(	SEP 29	M
Uanar	able Assistant Commissioner			ECHNOLOGY CENTER	Q	RECEIVE
for Pat					2003	
	ngton, D.C. 20231			70	دن	-
· · · · · · · · · · · · · · · · · · · ·			×():	28	٠:	
			•	$\sim$		

[x] The fee for submission of additional claims is calculated as shown below:

Sir:

For	TOTAL WITH NEW CLAIMS ADDED	TOTAL CURRENTLY ON FILE	CLAIMS PAID	RATE	CALCULATION
Total Claims	12	12	(Over 20)	x \$18	0
Independent Claims	3	3	(Over 3)	x \$84	0
MULTIPLE DEPENDENT CLAIM(S)				+ \$280	0
	LING BY SMALL ENTITY JFIED STATEMENT MUS	(note 37 C.F.R. §§ 1.9, 1.27 T BE ATTACHED	7, 1.28).	x ½	
TOTAL .			L	0	

In addition, the below-identified communications are su	omitted in the above-captioned application or proceeding		
[x] Response to Office Action	[ ] Petition for Extension of Time ( month)		

(with Claim Amendments) [ ] Terminal Disclaimer
[ ] Substitute Specification & marked-up copy [ ] Letter to Draftsperson w/ sheets of drawings

	[ ] Preliminary Amendment [ ] Assignment [ ] Petition under			
[]	Please charge my <b>Deposit Account Number</b> in the amount of to cover the fees for A duplicate copy of this paper is enclosed.			
[ ]	A check in the amount of \$ to cover the fee is enclosed.			
[x]	The Commissioner is hereby authorized to charge any additional fees associated with this communication including patent application filing fees and processing fees under 37 C.F.R. § 1.16 and 1.17, or credit overpayment to <b>Deposit Account Number 08-1480</b> .			
	Respectfully submitted,			
	Stanley P. Fisher			
	Registration Number 24,344  Marquez  Registration No. 34,672			
10.10.10	UD COMPETENT I D			

REED SMITH LLP

3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200 September 25, 2003

- 1 -IN THE UNITED STATES PATENT AND TRADEMARK OFFIC In re U.S. Patent Application of HASEGAWA et al. **Unit 2813** Application Number: 10/058,787 Examiner Filed: January 30, 2002 Dolan, Jennifer M SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, METHOD OF TESTING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND METHOD OF MANUFACTURING SEMICONDUCTOR INTEGRATED ) CIRCUIT DEVICE

Honorable Assistant Commissioner for Patents Washington, D.C. 20231

ATTORNEY DOCKET NO. ASAM.0038

RESPONSE AND AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

For:

This is in response to the Office Action dated September 9, 2003, the period for response to which will expire on October 9, 2003. Applicants hereby timely elect the continuing prosecution of Claims 1-8, without traverse.